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Mary R. Zimmerman

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

Hieu Van TRAN et al.

Examiner: Not yet assigned

Serial No.: Not yet assigned

Group Art Unit: Not yet assigned

Filing Date: August 13, 2001

For: **ARRAY ARCHITECTURE AND
OPERATING METHODS FOR
DIGITAL MULTILEVEL
NONVOLATILE MEMORY
INTEGRATED CIRCUIT SYSTEM**

PRELIMINARY AMENDMENT

Box Patent Application
Commissioner for Patents
Washington, DC. 20231

Sir:

Prior to examination of the above-identified application, which is a divisional of application serial number 09/231,928 filed on January 14, 1999, Applicants request the following amendments and cancellation and amendment of claims be entered.

09/231,928-001304